

USER MANUAL EAZY-CAL™ LVC-4000

LVDT Signal Conditioner





EAZY-CAL™ LVC-4000

LVDT Signal Conditioner

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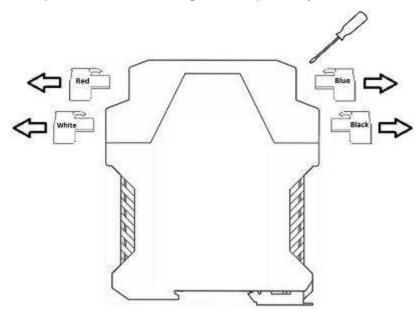


1 GENERAL DESCRIPTION

The EAZY-CAL™ LVC-4000 is a standalone signal conditioner, supporting a wide range of LVDTs, RVDTs and Half-Bridges and providing several choices of voltage, current, and digital RS-485 outputs. Push button calibration offers intuitive operation as compared to units with span and offset trim pots. Fault conditions such as a wire break on LVDT connections are indicated by blinking LEDs, output of an error voltage or current, and Error Flag Open Collector signal. The LVC-4000 operates from a 9 - 30VDC power supply and is housed in a polyamide DIN railmounted enclosure. Calibration instructions, terminal functions, LVDT connection diagram and DIP switch functions are printed on the side panels for convenience.

Synchronization to other signal conditioners is accomplished by a daisy chain connection to a synchronization bus. One unit will assume the Master function based on DIP switch priority setting. If a fault should occur, the next highest priority unit will take over as Master. With the use of the RS-485 port, a host computer is able to retrieve measurement data, receive operational status, perform remote calibration and perform hot swap reconfiguration.

Hot swapping installed LVC-4000 units is simplified by the pluggable terminal block feature. An LVC-4000 can be quickly replaced in a live system without the need to unscrew terminal connections or sever connections between other signal conditioners. The illustration below shows how the terminal plugs can be easily pried from their sockets using a small flat-head screwdriver. Once the newly replaced LVC-4000 is in position the terminal plugs are then pushed back into their matching color coded positions by hand.



The following sections describe the features and operation of the LVC-4000.

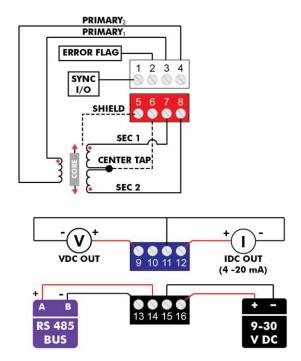
2 ELECTRICAL CONNECTIONS

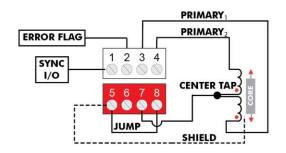
There are four color coded terminal plugs, each having 4 screw terminals. The 16 terminals are labeled 1 through 16. The terminal names and their function shown below:

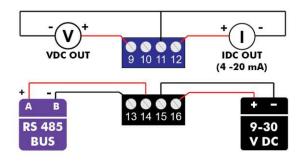
| Color | Terminal | Name | Function |
|-------|--|--------------|---|
| White | White 1 SYNC I/O Synchronization Master/Slave Input/Output | | Synchronization Master/Slave Input/Output |
| | 2 | ERROR FLAG | Error Flag Output |
| | 3 | PRI-1 | Primary Excitation to LVDT |
| | 4 | PRI-2 | Primary Excitation to LVDT |
| Red | 5 | SHIELD (GND) | Optional cable Shield connection |
| | 6 | SEC_CT | Optional Secondary CT conn. (n.c.) |
| | 7 | SEC-1 | Secondary signal from LVDT |
| | 8 | SEC-2 | Secondary signal from LVDT |

LVDT Signal Conditioner

| Blue | 9 | VDC OUT | Output Voltage |
|-------|----|------------|-----------------------|
| | 10 | n.c. | No Connection |
| | 11 | GND | VOUT / IOUT Return |
| | 12 | IDC OUT | Output Current |
| Black | 13 | RS-485 B | RS-485 Data - |
| | 14 | RS-485 A | RS-485 Data + |
| | 15 | -VIN (GND) | Supply Voltage Return |
| | 16 | +VIN | Supply Voltage In |







LVDT/LVRT Connection Diagram

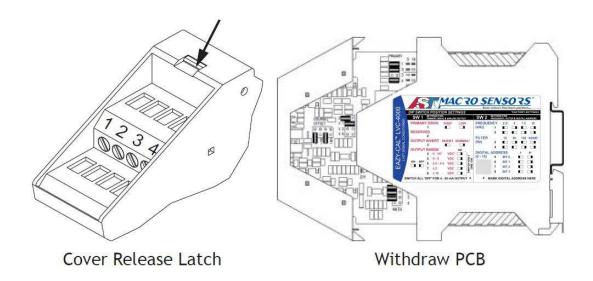
Half-Bridge Connection Diagram

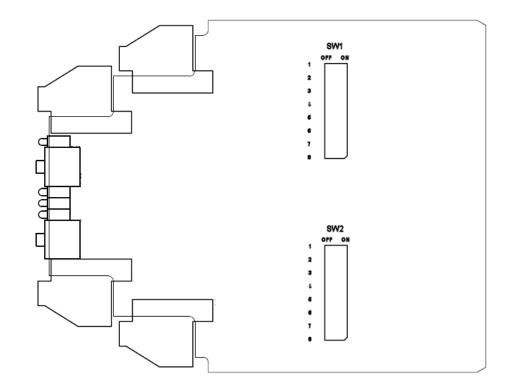
Note – select either voltage (factory default) or current output using DIP SW1.

3 DIP SWITCHES

There are two 8-section DIP switches on the PCB. The switches provide selection of Primary Level, Excitation Frequency, Bandwidth, Output Voltage Range Select, Output Invert, and RS-485 Bus Address.

To access the DIP switches, the front cover and PCB must be withdrawn from the housing. Use a screwdriver or similar tool to depress the top latch. The cover will spring forward. Repeat with the bottom latch, then gently pull the PCB out, as shown:





The following table shows the control settings of the DIP switches:

| FUNCTION | SWITCH - SECTION | | SETTING | 3 |
|--|---------------------|------------|---------|----------------|
| Primary Drive | | | LOW | <u>HIGH</u> |
| Select between HIGH (3Vrm)s & | SW1-1 | | OFF | ON * |
| LOW (1.5Vrms) Excitation Output | | | | |
| (Set to LOW if Primary impedance is $< 200\Omega$.) | | | | |
| Reserved | | | | |
| No Function | SW1-2 | | OFF * | ON |
| Output Invert | | | Normal | <u>Invert</u> |
| Reverse the direction of | SW1-3 | | OFF * | ON |
| Output vs. displacement | | | | |
| Output Range | SW1-4 | 0-10VDC * | | (Only 1 switch |
| Select one of five VDC | SW1-5 | 0-5VDC | | on at a time) |
| outputs or the IDC output. | SW1-6 | 0.5-4.5VDC | | |
| | SW1-7 | +/-5VDC | | |
| | SW1-8 | '+/-10VDC | | |
| | All Off: | 4-20 mADC | | |

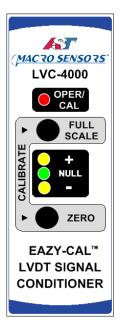
| Frequency Select one of four Excitation Frequencies | SW2-1 SW2-2 | 2.5KHz OFF * OFF * | <u>5KHz</u> ON OFF | 7.5Khz OFF ON | 10KHz ON ON |
|---|----------------------------------|-------------------------------------|---|---------------------|----------------------|
| Filter Select one of 4 filter cutoffs | SW2-3 SW2-4 | 1Hz OFF OFF | 10Hz ON OFF | 100Hz OFF ON | none ON * ON * |
| Bus Address Select one of 16 RS-485 Bus Address via a 4 switch binary number. See table for switch combinations. (Also used to select order of Synchronization Master/Slave order.) | SW2-5 SW2-6 SW2-7 SW2-8 | ADDR0 (Isb) ADDR1 ADDR2 ADDR3 (msb) | ON / OFF: 1 / 0 1 / 0 1 / 0 1 / 0 | | |

| BUS ADDR: | 00 * | 01 | 02 | 03 | 04 | 05 | 06 | 07 |
|-----------|------|-----|-----|-----|-----|-----|-----|-----|
| SW2-5: | OFF | ON | OFF | ON | OFF | ON | OFF | ON |
| SW2-6: | OFF | OFF | ON | ON | OFF | OFF | ON | ON |
| SW2-7: | OFF | OFF | OFF | OFF | ON | ON | ON | ON |
| SW2-8: | OFF | OFF | OFF | OFF | OFF | OFF | OFF | OFF |
| BUS ADDR: | 08 | 09 | 10 | 11 | 12 | 13 | 14 | 15 |
| SW2-5: | OFF | ON | OFF | ON | OFF | ON | OFF | ON |
| SW2-6: | OFF | OFF | ON | ON | OFF | OFF | ON | ON |
| SW2-7: | OFF | OFF | OFF | OFF | ON | ON | ON | ON |
| SW2-8: | ON | ON | ON | ON | ON | ON | ON | ON |

^{* -} Factory default settings.

4 FRONT PANEL

The front-facing side of the enclosure contains the push buttons and LED indicators which are used for calibrating the LVC-4000 to the LVDT.



| | | Front Panel Description |
|-------------------|------------|--|
| OPER / CAL LED | (Red) | On indicates Operating Mode, Blinking indicates Calibration Mode. |
| POSITION LEDs: | | During Calibration: |
| + | (Yellow) | '+' (Yellow) indicates core is above null position |
| NULL | (Green) | NULL (Green) indicates core is at null position |
| - | (Yellow) | '-' (Yellow) indicates core is below null position |
| | | During Normal Operation: Error Condition: '+' (Yellow) blinking indicates Primary Wire Break '-' (Yellow) blinking indicates Secondary Wire Break Synchronization State: '+' (Yellow) indicates Slave mode NULL (Green) indicates Master mode |
| FULL SCALE / ZERO | O Buttons: | During Normal Operation: Press both buttons for 3 seconds to enter Calibration Mode During Calibration: Press ZERO button to set core minimum endpoint position Press FULL SCALE button to set core maximum endpoint position Press both buttons for 3 seconds to abort Calibration Mode |

5 CALIBRATION PROCEDURE

Calibrating the LVC-4000 consists of entering Calibration Mode, finding the NULL (center) position of the LVDT, attaching the core to the object to be measured, and then moving the core to set the ZERO and FULL SCALE positions that will correspond to the minimum and maximum output voltage or current. The LVC-4000 returns to Operating Mode immediately after both positions have been set.

The basic calibration instructions are printed on the side of the LVC-4000 for reference.

Directions are as follows:

- Set the LVC-4000 DIP Switches for the desired application. The factory default settings provide a 0 - 10VDC output, and are typical for most standard LVDTs. Refer to section 3.0 for details.
- Connect the LVDT to the LVC-4000 as shown in the connection diagram and apply power. The OPER/CAL LED will light, along with the Green NULL LED (if the unit is in Master Sync mode), or the Yellow + LED (if the unit is in Slave mode). (3 minutes of warm-up time is recommended.)
- Enter the Calibration Mode by pressing and holding both buttons simultaneously for 3 seconds minimum. The OPER/CAL LED will begin
- Move the LVDT core to NULL position by observing the 3 Position LEDs. The + and LEDs indicate that the core is inserted or extended beyond the NULL, respectively.
- 5. Attach the core to the workpiece to be measured so that the workpiece center of motion is aligned with the LVDT NULL.
- Move workpiece to its minimum position and press the ZERO button. Wait for the Position LEDs to stop blinking. 6.
- Move workpiece to its maximum position and press the FULL SCALE button. The unit will exit Calibration Mode and operate with its new calibration. The OPER/CAL LED will be steady On.

5.1 Notes on Calibration

- It is important that the NULL position be set as close as possible to the center of the displacement range. Otherwise, non-linearity errors may result at or near the endpoint positions.
- The LVDT core MUST NOT protrude from the LVDT during the calibration, or erroneous calibration will result.
- Calibration may be performed by setting the FULL SCALE position first rather than ZERO position. Either end position may be on either side of NULL.
- Any portion of the LVDT specified displacement can be calibrated to produce a full output span. If the desired displacement is asymmetrical, set the endpoint that is farthest from NULL first.
- Swapping SEC-1 with SEC-2 or PRI-1 with PRI-2 LVDT connections will reverse the direction indicated by the Position LEDs.
- Calibration mode can be canceled by pressing the 2 buttons simultaneously for 3 seconds.
- Changing the cable length between the LVDT and LVC-4000 after calibration may alter the calibration. Re-calibration is recommended after changing the cable length.

6 RS-485 DIGITAL INTERFACE

Included on the LVC-4000 is a 2 wire multi-drop RS-485 communications port. This allows a host computer to perform remote calibration, receive operational status and retrieve measurement data. Data can be exchanged between one or more units and a PC with a USB-to-RS485 adapter attached. A terminal program (Hyperlink) or 3rd party software is required. Port parameters are 9600 bps, no parity, 8 data bits and 1 stop bit (9600, NP, 8,1). Data is ASCII.

6.1 RS-485 DIGITAL INTERFACE

Units respond to commands in the format:

:aa cmd param (CR)

where:

. . is the colon character,

is the Unit Address in decimal format 00..15, as set by the Digital Address DIP switches 'aa'

'cmd' is the command

is the optional command parameter 'param' '(CR) ' is a carriage Return character

A space character is required between the address and command and between the command and parameter. Commands are not case sensitive.

6.2 COMMANDS

The commands and their descriptions are as follows:

| Shows the operating state of the unit and displays a list of all of the significant parameters, as shown: Filter Select | | Command, Description | Syntax | Re | esponse |
|--|-----------|--|-----------------------|---------------------|-----------------------|
| Private Priv | getConfig | Shows the operating state of the unit and | :aa getConfig (CR) | Macro Sensors LV0 | C-4000 Configuration: |
| Serial No. Bus Address 0 Excitation Freq. 2.5KHz Filter Select NONE Invert NORM Output Range 10UT ADC Slope 4.048681 ADC Offset 301 INA Gain 2 ADC Range 1 Sync State MASTER ADC Range 1 INA GAIN = n Returns nominal analog output value in units of Output Range In Range | | displays a list of all of the significant | | H/W Version | REV |
| Bus Address 0 Excitation Freq. 2.5KHz Filter Select NONE Invert NORM Output Range IOUT ADC Slope 4.048691 ADC Offset 301 INA Gain 2 ADC Range 1 Sync State MASTER MASTER ADCS lope coefficient during hot swap reconfiguration. Set the ADC Slope coefficient during hot swap reconfiguration. Set the ADC Offset coefficient during hot swap reconfiguration. Set the ADC Range in CR ADC Range in Range | | parameters, as shown: | | F/W Version | |
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| Invert NORM Output Range 100T ADC Slope 4.048681 ADC Offset 301 INA Gain 2 ADC Range 1 Sync State MASTER ### MASTER ### MAS | | | | Excitation Freq. | 2.5KHz |
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| | getError | Returns error ststus. | :aa getError (CR) | · · | BREAK / SECOMDARY |
| help Lists all the available LVC-4000 commands. :aa help (CR) | tmpr | Displays the PCB Temperature °C | :aa tmpr (CR) | | |
| | help | Lists all the available LVC-4000 commands. | :aa help (CR) | | |

Note that the ADC Slope, ADC Offset, INA (Instrumentation Amplifier) Gain, and ADC Range parameters are computed during calibration, and can be downloaded into another unit. This allows for hot swap configuration, where a replacement unit can instantly function in place of the old unit without the need for rerunning the calibration process.

7 SYNCHRONIZATION

When multiple LVC-4000s share interconnecting wiring, interference may occur between them because of differences in excitation frequency. This may produce noise oscillations on the voltage or current outputs. In order to prevent this, the LVC-4000s can be synchronized with each

Synchronization of LVC-4000 signal conditioners is accomplished by connecting the SYNC I/O terminals of each unit into a daisy chain. One unit will assume Master Mode based on the priority set by the Digital Address DIP switches. With each unit set to a unique address, the lowest addressed unit becomes Master. All other units enter Slave Mode. If the current Master ceases to function, the next consecutively-addressed unit enters Master Mode without user intervention. If a unit is hot swapped or reconnected, it will enter Slave Mode. In an interconnected group of units, one and only one LVC-4000 is guaranteed to be Master, even if duplicate addresses exist.

Synchronization mode is indicated by the '+' (Yellow) LED for Slave Mode, and the NULL (Green) LED for Master Mode.

8 ERROR DETECTION AND INDICATION

The LVC-4000 detects and reports operational failures, including broken connections to the LVDT Primary and Secondary windings. Error conditions are indicated as follows:

LEDs:

Primary Open condition is indicated by a blinking '+' (Yellow) LED. Secondary Open condition is indicated by a blinking '-' (Yellow) LED

Error Flag:

The Error Flag is an Open Collector output signal requiring an external pullup resistor. The output is low (transistor turned on) during normal operation, and high (turned off) for any error condition.

Error Output Voltage or Current Level:

An error condition causes the LVC-4000 Voltage or Current Output to go to a level outside of the measurement range. For 0-10 VDC or 0-5 VDC ranges, the error voltage is -0.5 VDC and -0.25 VDC respectively. For 4-20 mADC range, the error current is 0 mADC.

9 LVC-4000 GENERAL SPECIFICATIONS

| Parameter | Value | Comment |
|---------------------------|---|---------|
| Power: | ! | |
| Input Voltage | 9-30 VDC, 90 mA max @ 24 VDC | |
| Excitation: | | |
| Primary Voltage (nominal) | 3 V _{RMS} (1.5 V _{RMS} selectable) | |
| Primary Current | 30 mA max | |
| Primary Impedance | 90Ω min (@ 1.5 V _{RMS}) | |
| Primary Frequency | 2.5 kHz, 5 kHz, 7.5 KHz or 10 kHz | |
| Signal Output: | | |
| Voltage Output | 0-10 VDC, 0-5 VDC, 0.5-4.5 VDC, ±5 VDC, or ±10 VDC, selectable | |
| Current Output | 4 - 20 mADC | |
| Loop resistance | 500Ω Max | |
| Frequency Response | 500Hz Max | |
| Low Pass Filter | 1Hz, 10Hz, 100Hz or none, selectable | |
| Output Voltage Noise | 1 mV _{RMS} max (2.5KHz Excitation, no Filter) 2 mV _{RMS} max (10KHz Excitation, no Filter) | |
| Output Current Noise | 10 μA _{RMS} max (2.5KHz Excitation, no Filter) 20 μA _{RMS} max (10KHz Excitation, no Filter) | |
| Output Non-Linearity | < ±0.1% of FSO | |

TE CONNECTIVITY SENSORS /// EAZY-CAL™ LVC-4000

LVDT Signal Conditioner

| Temp. Co. Gain | <0.02% FRO/°C | |
|-----------------------------|---|--|
| Temp. Co. Offset | <0.02% FRO/°C | |
| Operating Temperature Range | -20°C to 75°C | |
| Enclosure | 4.5 x 3.9 x .9 in., Green | |
| EMC Compliance | Emissions: EN55011:2007 Immunity: EN61000-4-2:2009, EN61000-4-4:2004, EN61000- 4-6:2009, EN61000-4-3:2010+A2:2010 | |
| ures: | | |
| Synchronization Capability | Master/Slave Synchronization via single wire bus. | |
| Automatic Calibration | Via Front Panel Push Button or RS-485 command | |
| Null Detection | Via Front Panel LEDs or RS-485 command | |
| Digital interface | RS-485 2-wire multi-drop network, 16 addresses selectable | |
| Error Detection | Primary or Secondary Wire Break Detect, indicated by LED, Error Flag, and Error Output Level | |
| Error Flag | Open collector output, 50 mA, 30 V DC max. | |

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